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12/02/2003

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(Depositor's name)
<i>Loren D. Pearson</i> LOREN DONALD PEARSON (Signature)
REG. NO. 42,987
March 2, 2004 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/943,589	08/30/2001	Rainald Sander	WMP-IFT631	1967

TITLE OF INVENTION: CIRCUIT CONFIGURATION FOR DETECTING THE CURRENT IN A LOAD TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	03/02/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
PATEL, PARESH H	2829	324-765000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Laurence A. Greenberg
2 Werner H. Stemer
3 Ralph E. Locher

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Infineon Technologies AG

München, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent);

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LOREN DONALD PEARSON (Date)
REG. NO. 42,987

3/2/04

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